



St. MARTIN'S ENGINEERING COLLEGE
An Autonomous Institute
Dhulapally, Secunderabad – 500100
NBA & NAAC A+ Accredited
Department of Electronics & Communication Engineering



TIME – TABLE

Year : IV B.Tech Semester : II
Section : A Academic Year : 2021-2022
Room No : 1220 Block Name : MG Block
W.e.f : 02/03/2022 Class In charge : G.POSHAMALLU

HOUR	9:20 AM to 10:10 AM	10:10 AM to 11:00 AM	11:00 AM to 11:50 AM	11:50 AM to 12:40 PM		1:20 PM to 2:10 PM	2:10 PM to 3:00 PM	3:00 PM to 3:50 PM
DAY	1	2	3	4		5	6	7
MON	RS CH	SOCA GP	RS CH	SOCA GP	L U N C H B R E A K	SL SS	RS CH	SL SS
TUE	SOCA GP	RS CH	SL SS	RS CH		SL SS	SOCA GP	SL SS
WED	SL SS	RS CH	SOCA GP	SL SS		SOCA GP	RS CH	SOCA GP
THU	PROJECT STAGE-II					PROJECT STAGE-II		
FRI	PROJECT STAGE-II					PROJECT STAGE-II		
SAT	PROJECT STAGE-II					PROJECT STAGE-II		

RS : Radar Systems
SOCA : System On Chip Architecture
SL : Scripting Language
PROJECT STAGE-2 : Project Stage-II

CH : C. Harini
GP : G. Poshamallu
SS : S. Sravanthi
KN : K. Nishakar

[Signature]
CHIEF IN CHARGE (TIME TABLES)

[Signature]
HOD, ECE

[Signature]
PRINCIPAL

Head of the Department
Department of Electronics and
Communication Engineering
St. Martin's Engineering College
Dhulapally, Secunderabad, Telangana-500100.

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Survey No. 98 & 100, Dhulapally (V)
Dundiga-Gandimaisamma (M), Medchal-Maikajiri
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TIME – TABLE

Year : IV B.Tech Semester : II
 Section : B Academic Year : 2021-2022
 Room No : 2212 Block Name : MV Block
 W.e.f : 02/03/2022 Class Incharge : K. VINEELA

HOUR	9:20 AM to 10:10 AM	10:10 AM to 11:00 AM	11:00 AM to 11:50 AM	11:50 AM to 12:40 PM		1:20 PM to 2:10 PM	2:10 PM to 3:00 PM	3:00 PM to 3:50 PM
DAY	1	2	3	4		5	6	7
MON	RS BSS	SOCA KV	RS BSS	SOCA KV	L U N C H B R E A K	SL GK	SOCA KV	SL GK
TUE	SOCA KV	RS BSS	SL GK	RS BSS		RS BSS	SL GK	SOCA KV
WED	SL GK	RS BSS	SOCA KV	RS BSS		SL GK	SOCA KV	SL GK
THU	PROJECT STAGE-II					PROJECT STAGE-II		
FRI	PROJECT STAGE-II					PROJECT STAGE-II		
SAT	PROJECT STAGE-II					PROJECT STAGE-II		

RS : Radar Systems
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 PROJECT STAGE-2 : Project Stage-II

BSS : B. Shiny Sucharitha
 KV : K. Vineela
 GK : G. Kishore
 KK : K. Karthik

CHIEF IN CHARGE (TIME TABLES)

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TIME – TABLE

Year : IV B.Tech Semester : II
 Section : C Academic Year : 2021-2022
 Room No : 2204 Block Name : MV Block
 W.e.f : 02/03/2022 Class Incharge : P. KIRANMAYEE

HOUR	9:20 AM to 10:10 AM	10:10 AM to 11:00 AM	11:00 AM to 11:50 AM	11:50 AM to 12:40 PM		1:20 PM to 2:10 PM	2:10 PM to 3:00 PM	3:00 PM to 3:50 PM
DAY	1	2	3	4		5	6	7
MON	SOCA PK	SL DRL	RS KDV	SL DRL	L U N C H B R E A K	SOCA PK	RS KDV	SOCA PK
TUE	SOCA PK	SL DRL	SOCA PK	RS KDV		SL DRL	RS KDV	SL DRL
WED	RS KDV	SOCA PK	SL DRL	SOCA PK		RS KDV	SL DRL	RS KDV
THU	PROJECT STAGE-II					PROJECT STAGE-II		
FRI	PROJECT STAGE-II					PROJECT STAGE-II		
SAT	PROJECT STAGE-II					PROJECT STAGE-II		

RS : Radar Systems	KDV : K. Divya Vani
SOCA : System On Chip Architecture	PK : P. Kiranmayee
SL : Scripting Language	DRL : D. Rama Lingam
PROJECT STAGE-2 : Project Stage-II	PP : P. Pushpa


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TIME – TABLE

Year : IV B.Tech Semester : II
 Section : D Academic Year : 2021-2022
 Room No : 2213 Block Name : MV Block
 W.e.f : 02/03/2022 Class Incharge : GABBETA RAMESH

HOUR	9:20 AM to 10:10 AM	10:10 AM to 11:00 AM	11:00 AM to 11:50 AM	11:50 AM to 12:40 PM	L U N C H B R E A K	1:20 PM to 2:10 PM	2:10 PM to 3:00 PM	3:00 PM to 3:50 PM
DAY	1	2	3	4		5	6	7
MON	RS GR	SL MVR	SOCA CKR	SL MVR		RS GR	SOCA CKR	RS GR
TUE	SOCA CKR	RS GR	SOCA CKR	RS GR		SL MVR	SOCA CKR	SL MVR
WED	SL MVR	SOCA CKR	SL MVR	SOCA CKR		RS GR	SL MVR	RS GR
THU	PROJECT STAGE-II					PROJECT STAGE-II		
FRI	PROJECT STAGE-II					PROJECT STAGE-II		
SAT	PROJECT STAGE-II					PROJECT STAGE-II		

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GR : Gabbeta Ramesh
 CKR : Chandan Kumar Roy
 MVR : M. Vishnuvardhan Reddy
 GR : Gabbeta Ramesh

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